

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Koichi Sato et al.

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: APPARATUS AND METHOD FOR ESTIMATING POWER CONSUMPTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

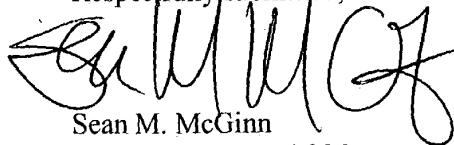
Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner. In compliance with the concise explanation requirement under 37 CFR §1.98(a)(3), the relevance of these documents is discussed on page 2 of the subject application. Further, an English-language Abstract is attached to one of the references.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,



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(Use several sheets if necessary)

NEG-299 US

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[illegible][illegible]

		"C++ Simulator for 'Classmate' for pre-verification on SOC", Hidefumi Kurokawa, Technical Report of IEICE, VLH98-46, pp. 17-24.

DATE CONSIDERED

SHEET 1 OF 1